

FIG.1.

Octet	1	2	3	4	5	6	7	8	9	10	11	12
CLIP1	ATM HEADER											
CLIP2												
Octet	13	14	15	16	17	18	19	20	21	22	23	24
CLIP1	XX	45			LENGTH			D			P	
CLIP2			P			IP SRC ADDRESS			IP DST ADDR-			
Octet	25	26	27	28	29	30	31	32	33	34	35	36
CLIP1		IP SRC ADDRESS			IP DST ADDRESS			SRC PORT DST				
CLIP2	ESS	SRC PORT	DST PORT					UD				
Octet	37	38	39	40	41	42	43	44	45	46	47	48
CLIP1	PORT			UD								
CLIP2									TD			
Octet	49	50	51	52	53							
CLIP1												
CLIP2												

FIG.2.

FIG. 3.

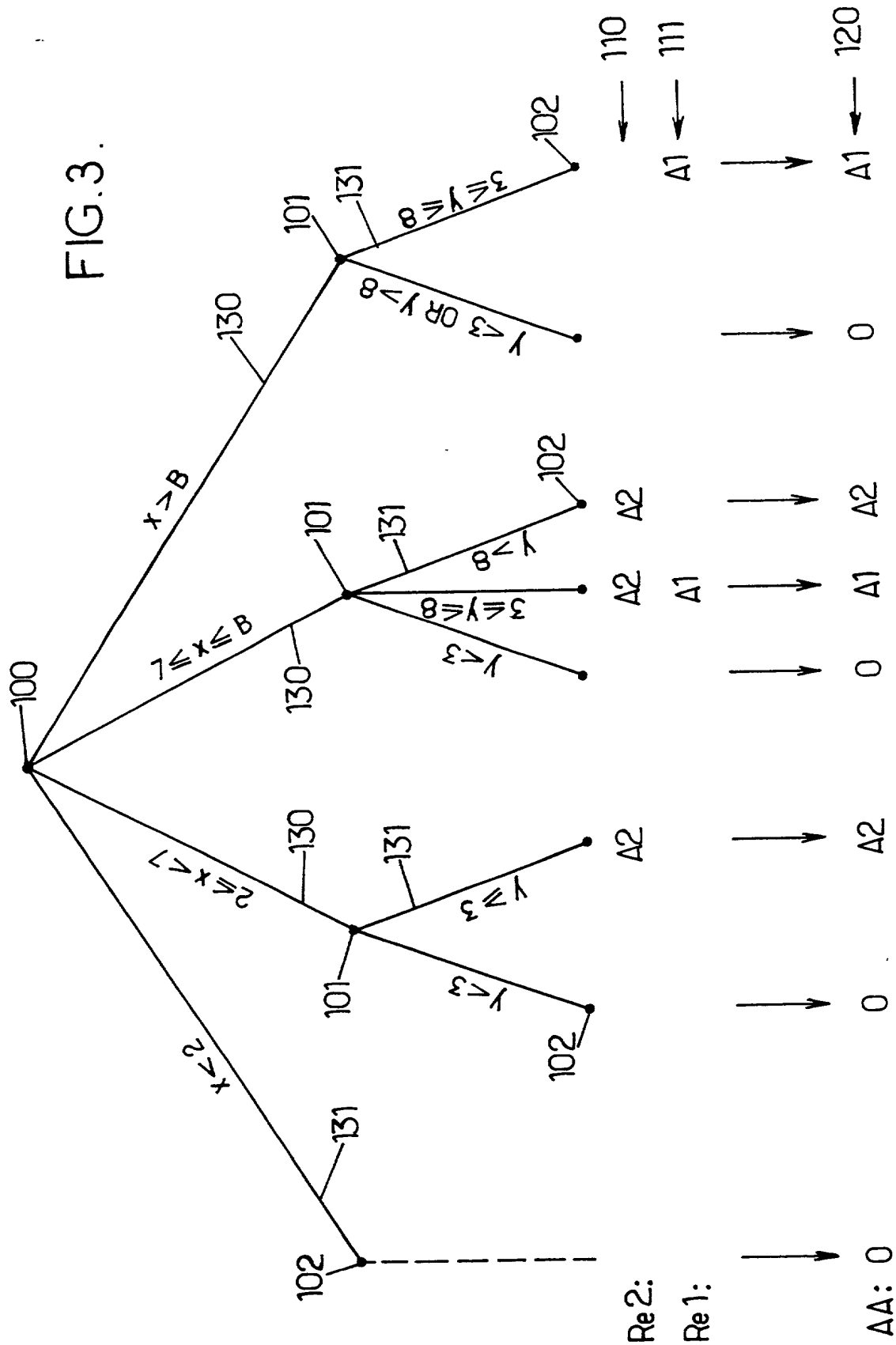


FIG. 4.

```

graph TD
    100((100)) -- "Y < 3" --> 102_1((102))
    100 -- "Y >= 8" --> 103_1((103))
    102_1 -.-> AA_0[AA: 0]
    103_1 -- "x < 2" --> 102_2((102))
    103_1 -- "x >= 2" --> 103_2((103))
    102_2 --> 0_1[0]
    103_2 -- "x < B" --> 102_3((102))
    103_2 -- "2 <= x <= B" --> 103_3((103))
    103_2 -- "x > B" --> 102_4((102))
    102_3 --> 110[110]
    103_3 --> 111[111]
    102_4 --> 120[120]
    
```

Re 2:  
Re 1:  
AA: 0

FIG. 5.

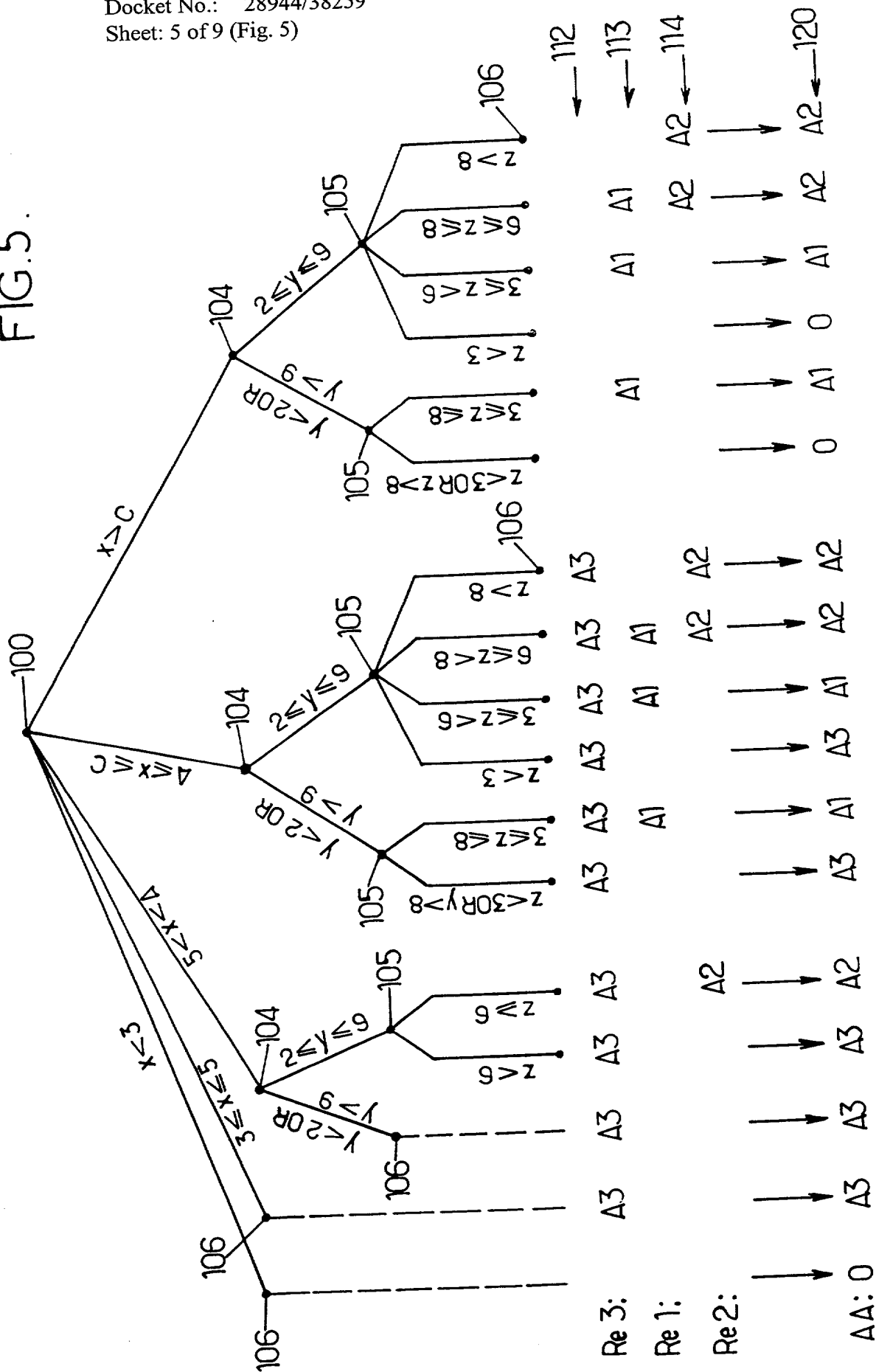


FIG. 6.

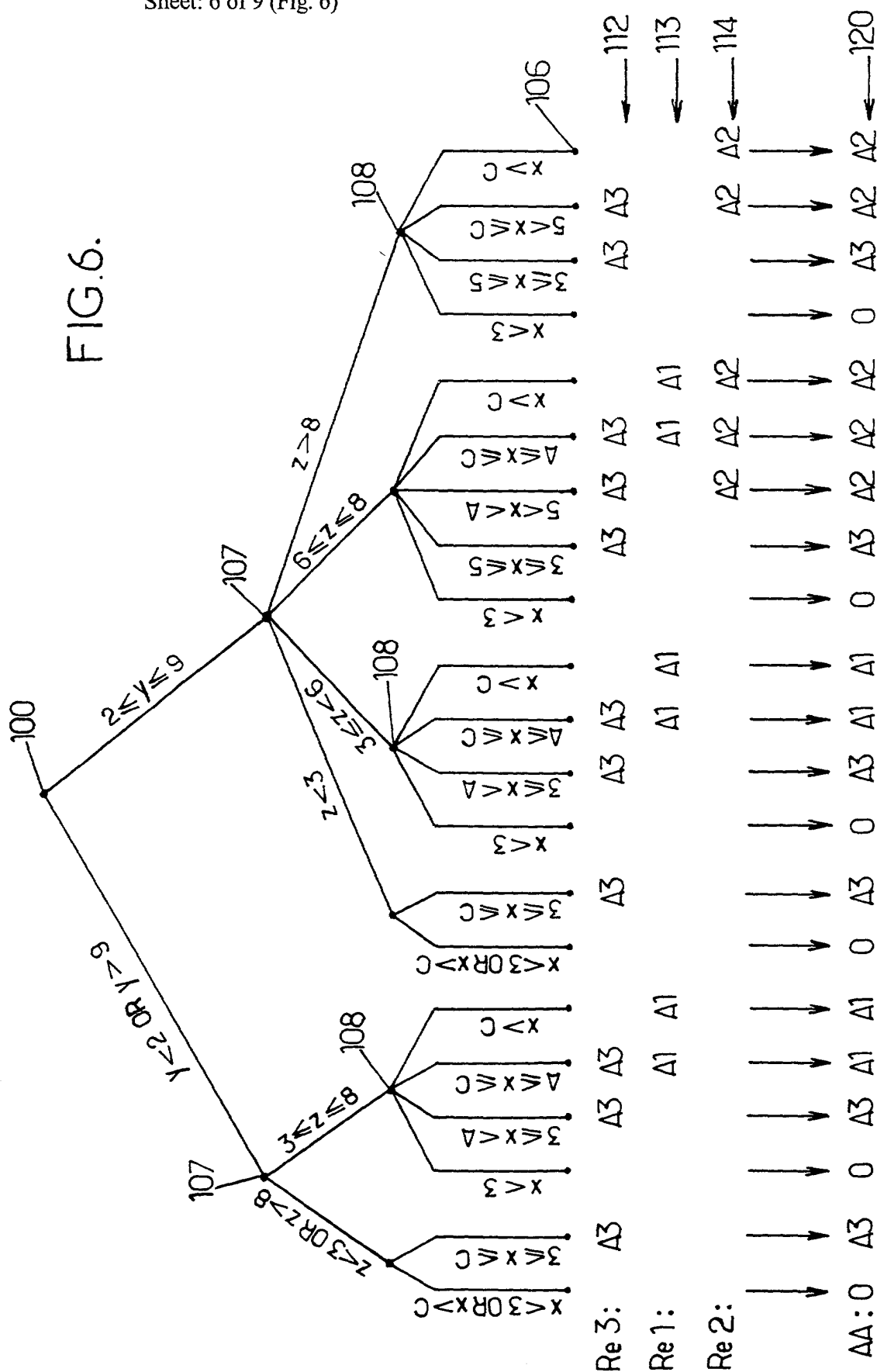
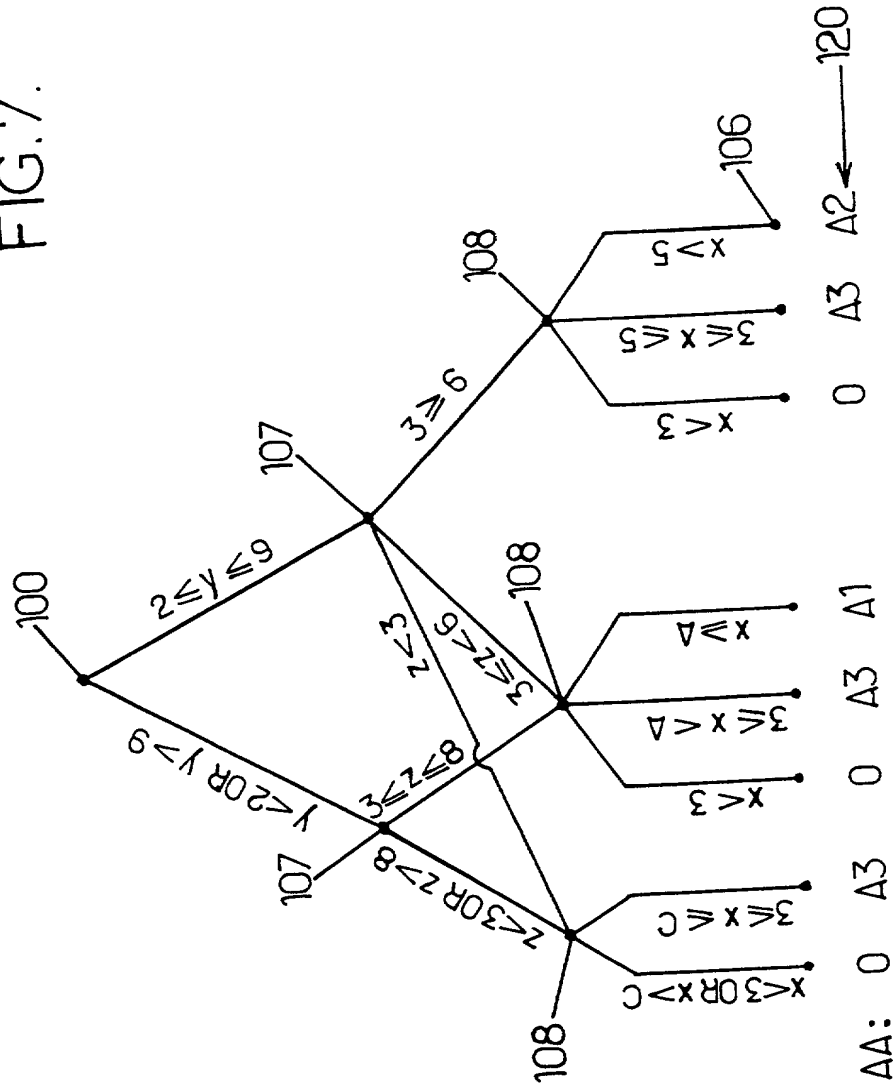


FIG. 7.



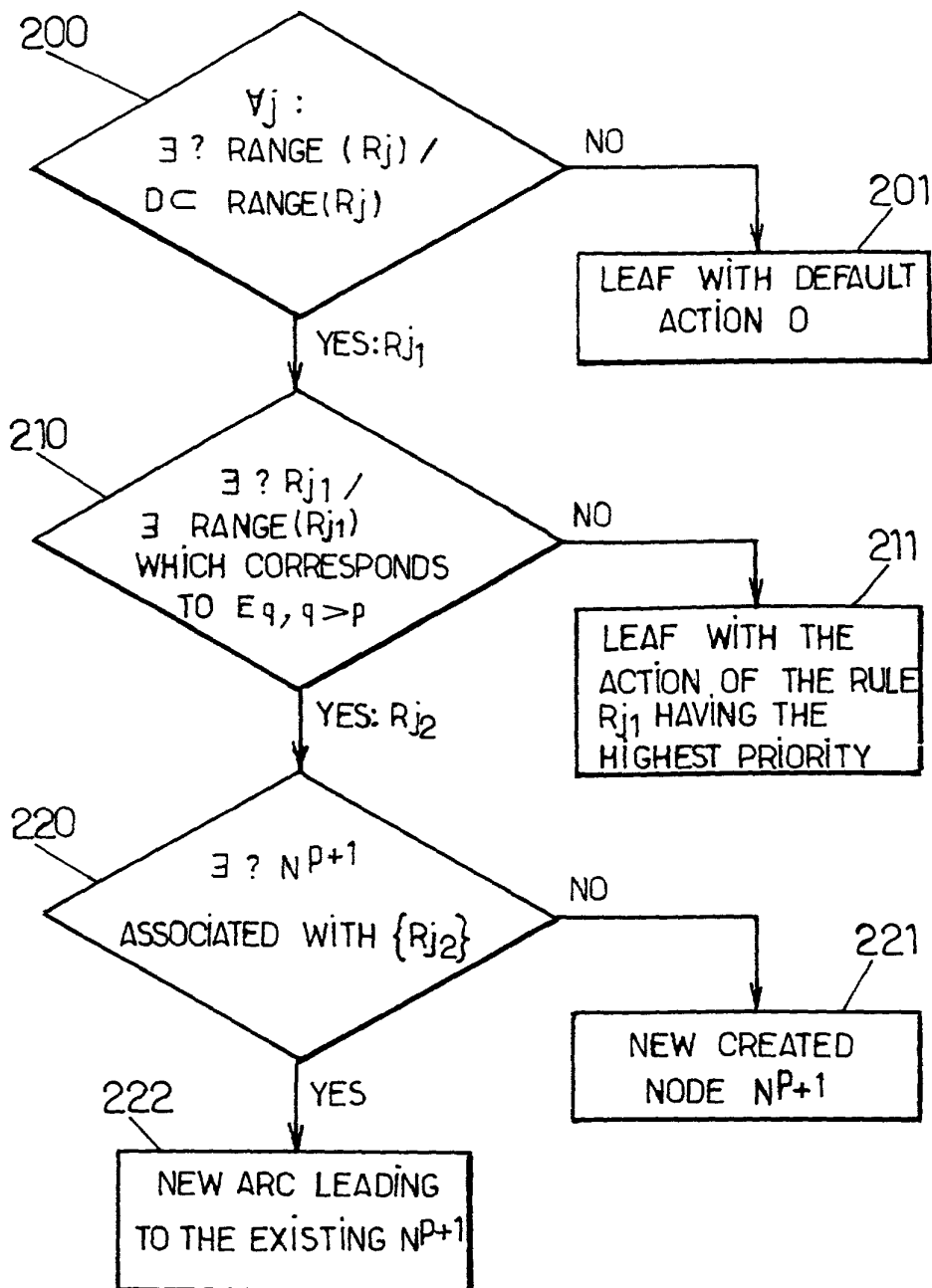


FIG. 8.



